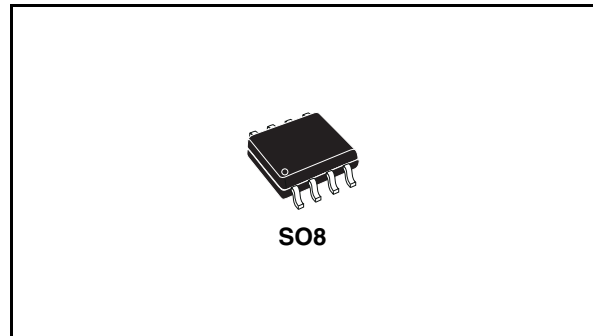


ISO 9141 interface
Features

- Operating power supply voltage range
 $4.5\text{ V} \leq V_S \leq 36\text{ V}$ (40 V for transients)
- Reverse supply (battery) protected down to
 $V_S \geq -24\text{ V}$
- Standby mode with very low current consumption
 $I_{S_{SB}} \leq 1\text{ mA}$ @ $V_{CC} \leq 0.5\text{ V}$
- Low quiescent current in off condition
 $I_{S_{OFF}} = 120\text{ }\mu\text{A}$
- TTL compatible TX input
- Bidirectional K-I/O pin with supply voltage dependent input threshold
- Overtemperature shut down function Selective to K-I/O pin
- Wide input and output voltage range
 $-24\text{ V} \leq V_K \leq V_S$
- K output current limitation, typ. $I_K = 60\text{ mA}$
- Defined OFF output status in undervoltage condition and V_S or GND interruption
- Controlled output slope for low EMI



- High input impedance for open V_S or GND connection
- Defined output ON status of LO or RX for open LI or K inputs
- Defined K output OFF for TX input open
- Integrated pull up resistors for TX, RX and LO
- EMI robustness optimized

Description

The L9637 is a monolithic integrated circuit containing standard ISO 9141 compatible interface functions.

Table 1. Device summary

Order code	Package	Packing
L9637D	SO8	Tube
L9637D013TR	SO8	Tape and reel
E-L9637D ⁽¹⁾	SO8	Tube
E-L9637D013TR ⁽¹⁾	SO8	Tape and reel

1. Device in ECOPACK® package (see [Section 4: Package information on page 13](#)).

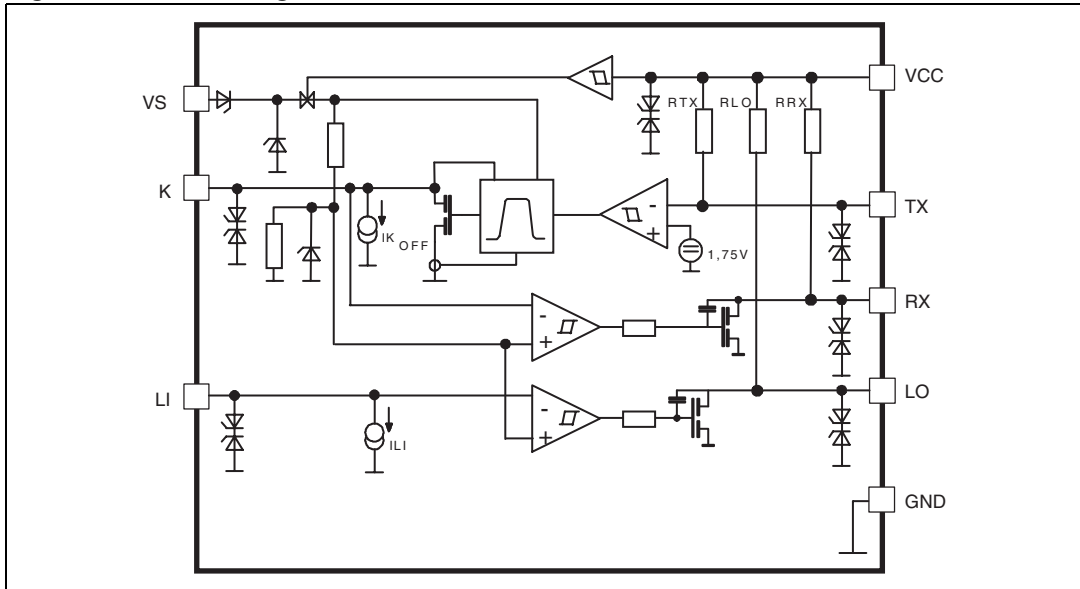
Contents

1	Block diagram and pin description	3
1.1	Block diagram	3
1.2	Pin description	3
2	Electrical specification	4
2.1	Absolute maximum ratings	4
2.2	Thermal data	4
2.3	Electrical characteristics	4
3	Functional description	8
4	Package information	12
5	Revision history	13

1 Block diagram and pin description

1.1 Block diagram

Figure 1. Block diagram



1.2 Pin description

Figure 2. Pin connection (top view)

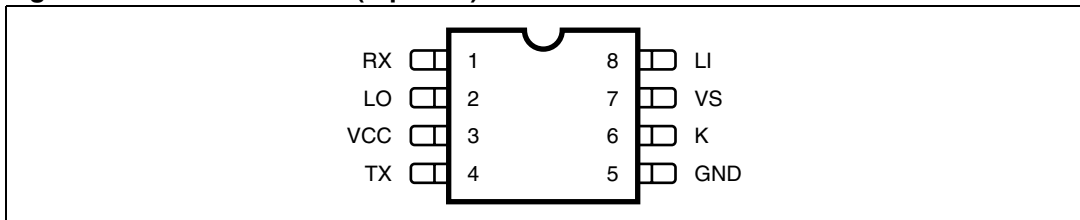


Table 2. Pin description

N.	Name	Function
1	RX	Output for K as input
2	LO	Output L comparator
3	VCC	Stabilized voltage supply
4	TX	Input for K as output
5	GND	Common GND
6	K	Bidirectional I/O
7	VS	Supply voltage
8	LI	Input L comparator

2 Electrical specification

2.1 Absolute maximum ratings

Table 3. Absolute maximum ratings (No damage or latch)

Symbol	Parameter	Value	Unit
V_S	Supply voltage ISO transients $t = 400 \text{ ms}$	-24 to +36 -24 to +40	V
V_{CC}	Stabilized voltage	-0.3 to +7	V
$\Delta V_S/d_t$	Supply voltage transient	-10 to +10	V/ μs
$V_{LI, K}$	Pin voltage	-24 to V_S	V
$V_{LO, RX, TX}$		-24 to V_{CC}	V

Note: Max. ESD voltages are $\pm 2\text{kV}$ with human body model $C = 100\text{pF}$, $R = 1.5\text{k}$ corresponds to maximum energy dissipation 0.2mJ according to MIL883C.

2.2 Thermal data

Table 4. Thermal data

Symbol	Parameter	Min.	Typ.	Max.	Unit
T_{JSDon}	Temperature K shutdown switch on threshold	160		200	$^{\circ}\text{C}$
T_{JSDoff}	Temperature K shutdown switch off threshold	150		200	$^{\circ}\text{C}$
$R_{th j-amb}$	Thermal steady state junction to ambient resistance	130	155	180	$^{\circ}\text{C}/\text{W}$

2.3 Electrical characteristics

Table 5. Electrical characteristics

(The electrical characteristics are valid within the below defined operating conditions, unless otherwise specified. The function is guaranteed by design until T_{JSDon} temperature shutdown switch-on-threshold).

Symbol	Parameter	Test condition	Min.	Typ.	Max.	Unit
V_S	Supply voltage		4.5		36	V
V_{CC}	Stabilized voltage		3		7	V
T_j	Junction temperature		-40		150	$^{\circ}\text{C}$
I_{CC}	Supply V_{CC} current	$V_{CC} \leq 5.5\text{V}; V_{LI}, V_{TX} = 0\text{V}$		1.4	2.3	mA
		$V_K \geq V_{K_{high}}; V_{LI} \geq V_{LI_{high}}$ $V_{TX} = V_{CC} @ V_{CC} \leq 5.5\text{V}$	-5	<1	5	μA

Table 5. Electrical characteristics (continued)

(The electrical characteristics are valid within the below defined operating conditions, unless otherwise specified. The function is guaranteed by design until T_{JSDon} temperature shutdown switch-on-threshold).

Symbol	Parameter	Test condition	Min.	Typ.	Max.	Unit
I_{SON}	Supply V_S Current	$V_S \leq 16V$; V_{LI} , $V_{TX} = 0V$		1.2	3	mA
I_{SOFF}		$V_K \geq V_{K_{high}}$; $V_{LI} \geq V_{LI_{high}}$ $V_{TX} \geq V_{TX_{high}}$ @ $V_S \leq 12V$		120	220	μA
I_{SSB}		$V_{CC} \leq 0.5V$ @ $V_S \leq 12V$		<1		μA
$V_{K_{low}}$	Input voltage low state	RX output status LOW $4.5V \leq V_S \leq 18V$	-24		$0.45V_S$	V
		RX output status LOW $18V < V_S$	-24		8	V
$V_{K_{high}}$	Input voltage high state	RX output status HIGH $4.5V \leq V_S \leq 18V$	$0.55V_S$		V_S	V
		RX output status HIGH $18V < V_S$	12		V_S	V
$V_{K_{hys}}$	Input threshold hysteresis	$V_{K_{high}} - V_{K_{low}}$		$0.025 V_S$	0.8	V
I_{Koff}	Input current	@ $V_{TX} \geq V_{TX_{high}}$ $V_K \leq V_S$; $V_S, V_{CC} \geq 0$ or $V_S, V_{CC} = \text{open}$	-5	4	25	μA
$R_{K_{ON}}$	Output ON impedance	@ $V_S \geq 6.5V$ $V_{TX} \leq V_{TX_{low}}$ $I_K \geq 7mA$ ⁽¹⁾		10	30	Ω
$I_{K_{SC}}$	Short circuit current		30	60	100	mA
$V_{TX_{low}}$	Input voltage LOW state		-24		1	V
$V_{TX_{high}}$	Input voltage HIGH state		2.5		V_{CC}	V
$R_{RX_{ON}}$ $R_{LO_{ON}}$	Output ON impedance	$V_K \leq V_{K_{low}}$; $V_{LI} \leq V_{LI_{low}}$ $V_S \geq 6.5V$ $I_{RX, LO} \geq 1mA$		40	90	Ω
$I_{RX_{SC}}$ $I_{LO_{SC}}$	Output short circuit current		9	20	35	mA
V_{RX_H} V_{LO_H}	Output voltage HIGH state	$10M\Omega \leq R_{LRX}$ $10M\Omega \leq R_{LLO}$	$V_{CC} - 0.25$	$V_{CC} - 0.1$	$V_{CC} -$	V
RLO RRX	Output pull-up resistance	Output status = (HIGH) $-0.15V \leq V_{LO} \leq V_{CC} + 0.15V$ $-0.15V \leq V_{RX} \leq V_{CC} + 0.15V$	5	10	20	k Ω
RTX	Input pull up resistance	$-0.15V \leq V_{TX} \leq V_{CC} + 0.15V$	10	20	40	k Ω
$V_{LI_{low}}$	Input voltage LOW state	LO output status LOW $4.5V \leq V_S \leq 18V$	-24		$0.45V_S$	V
		LO output status LOW $18V < V_S$	-24		8	V

Table 5. Electrical characteristics (continued)

(The electrical characteristics are valid within the below defined operating conditions, unless otherwise specified. The function is guaranteed by design until T_{JSDon} temperature shutdown switch-on-threshold).

Symbol	Parameter	Test condition	Min.	Typ.	Max.	Unit
V_{Lhigh}	Input voltage HIGH state	LO output status HIGH $4.5V \leq V_S \leq 18V$ LO output status HIGH $18V < V_S$	$0.55V_S$ 12		V_S	V
V_{LIhys}	Input threshold hysteresis	$V_{LIhigh} - V_{LIlow}$		$0.025V_S$	0.8	V
ILI	Input current	$V_{LI} \leq V_S$, $V_{CC} \geq 0$ or V_S , $V_{CC} = open$	-5	4	25	μA
$C_{Ki,LO,RX}$	Internal output capacities				20	pF
f_{LI-LO} f_{K-RX} f_{TX-K}	Transmission frequency	$9V < V_S < 16V$ (external loads) $R_{KO} = 510\Omega$, $C_K \leq 1.3nF$ in active mode see Figure 5	50	100		kHz
t_{rLI-LO} t_{rK-RX} t_{rTX-K}	Rise time	for the definition of t_r , t_f see Figure 3		2	6	μs
t_{fLI-LO} t_{fK-RX} t_{fTX-K}	Fall time	$9V < V_S < 16V$ (external loads) $R_{KO} = 510\Omega$, $C_K \leq 1.3nF$		2	6	μs
$t_{OFF,LI-LO}$ $t_{OFF,K-RX}$ $t_{OFF,TX-K}$	Switch OFF time	for the definition of t_{on} , t_{OFF} see Figure 3 .		4	17	μs
$t_{ON,LI-LO}$ $t_{ON,K-RX}$ $t_{ON,TX-K}$	Switch ON time	$9V < V_S < 16V$ (external loads) $R_{KO} = 510\Omega$, $C_K \leq 1.3nF$ (inactive mode see Figure 5)		4	17	μs

1. For output currents lower than this value a series protection diode can become active. See also [Figure 8](#) and [9](#).

Figure 3. Input to output timings and output pulse shape

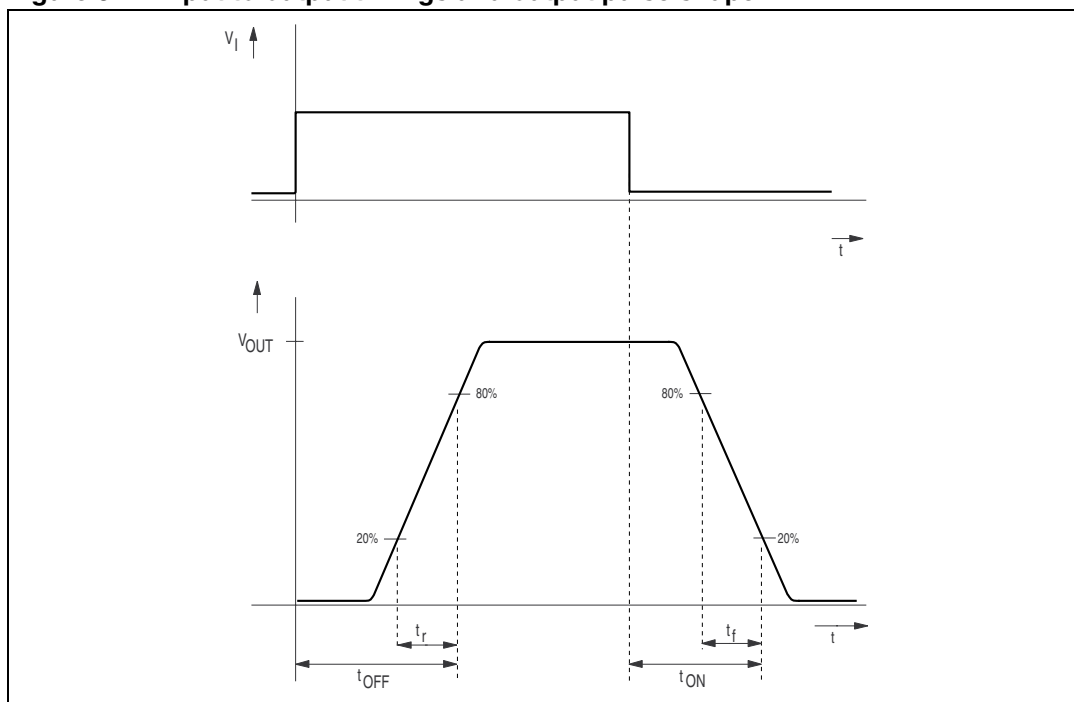
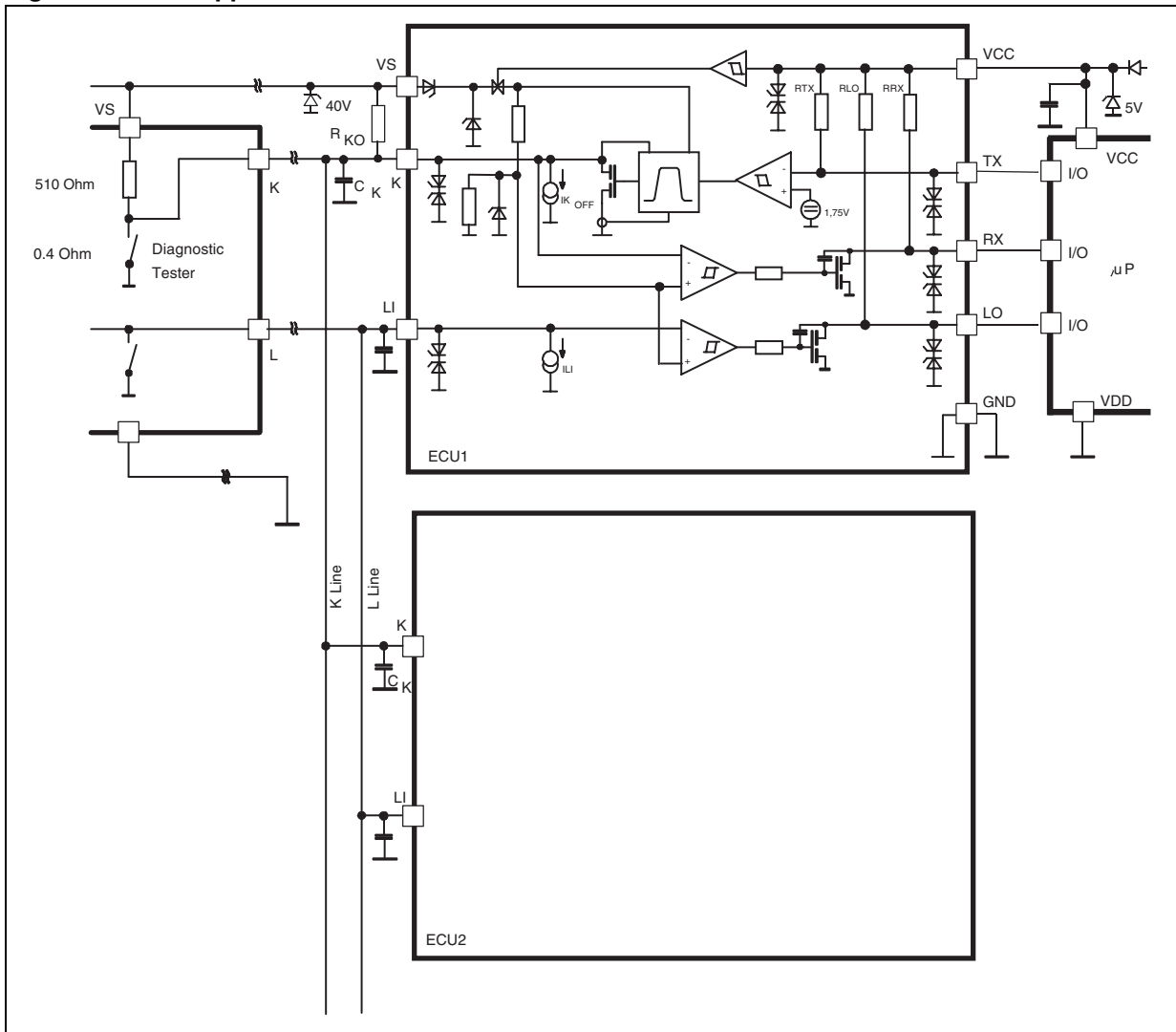


Figure 4. ISO application circuit



3 Functional description

The L9637 is a monolithic bus driver designed to provide bidirectional serial communication in automotive diagnostic applications according to the specification "Diagnostic Systems ISO9141".

The device provides a bidirectional link, called K, to the V_{Bat} related diagnosis bus. It also includes a separate comparator L which is also able to be linked to the V_{Bat} bus. The input TX and output RX of K are related to V_{CC} with her integrated pull up resistances. Also the L comparator output LO has a pull up resistance connected to V_{CC} .

The maximum external pull up resistance at K related to V_S should not be higher than $R_{KO} \leq 5 \text{ k}\Omega$ to achieve clear output ON conditions.

All V_{Bat} bus defined inputs LI and K have supply voltage dependent thresholds together with sufficient hysteresis to suppress line spikes. These pins are protected against overvoltages, shorts to GND and V_S and can also be driven beyond V_S and GND.

These features are also given for TX, RX and LI only taking into account the behavior of the internal pull up resistances. The thermal shut down function switches OFF the K output if the chip temperature increases above the thermal shut down threshold. To reactivate K again the temperature must decrease below the K switch ON temp. To achieve no fault for V_S undervoltage conditions the outputs will be switched OFF and stay at high impedance.

The device is also protected against reverse battery condition. During lack of V_S or GND all pins shows high impedance characteristic. To realize a lack of the V_S related bus line LI and K the outputs LO and RX shows defined ON status.

Suppressing all 4 classes of "Schaffner" signals all pins can be load with short energy pulses of max. $\pm 0.2 \text{ mJ}$. All these features together with a high possible baud rate $> 50 \text{ Kbaud}$, controlled output slopes for low EMI, a wide power supply voltage range and a very small quiescent current during OFF (TX LI K=High) condition $I_{Soff \text{ typ}} \leq 120 \mu\text{A}$, and a real standby function with zero power consumption $I_{S \text{ SB typ}} \leq 1 \mu\text{A}$ during system de powering $V_{CC} \leq 0.5 \text{ V}$ make this device high efficient for automotive bus system.

After wake up of the system from OFF or SB condition the first output signal will have an additional delay time $t_{d \text{ typ}} \leq 5 \mu\text{s}$ see also [Figure 5](#).

The typical output voltage behavior for the K, LO, RX outputs as a function of the output current is shown in [Figure 6](#). [Figure 7](#) shows a waveform of the output signal when the low level changes from $R_{ON} * I_{OUT}$ to $I_{OUT} * 2 * R_{ON} + U_{BE}$ state. This variation occurs due to too low output current or after a negative transient forced to the output or to the supply voltage line.

Figure 5. Typical timing for mode transitions

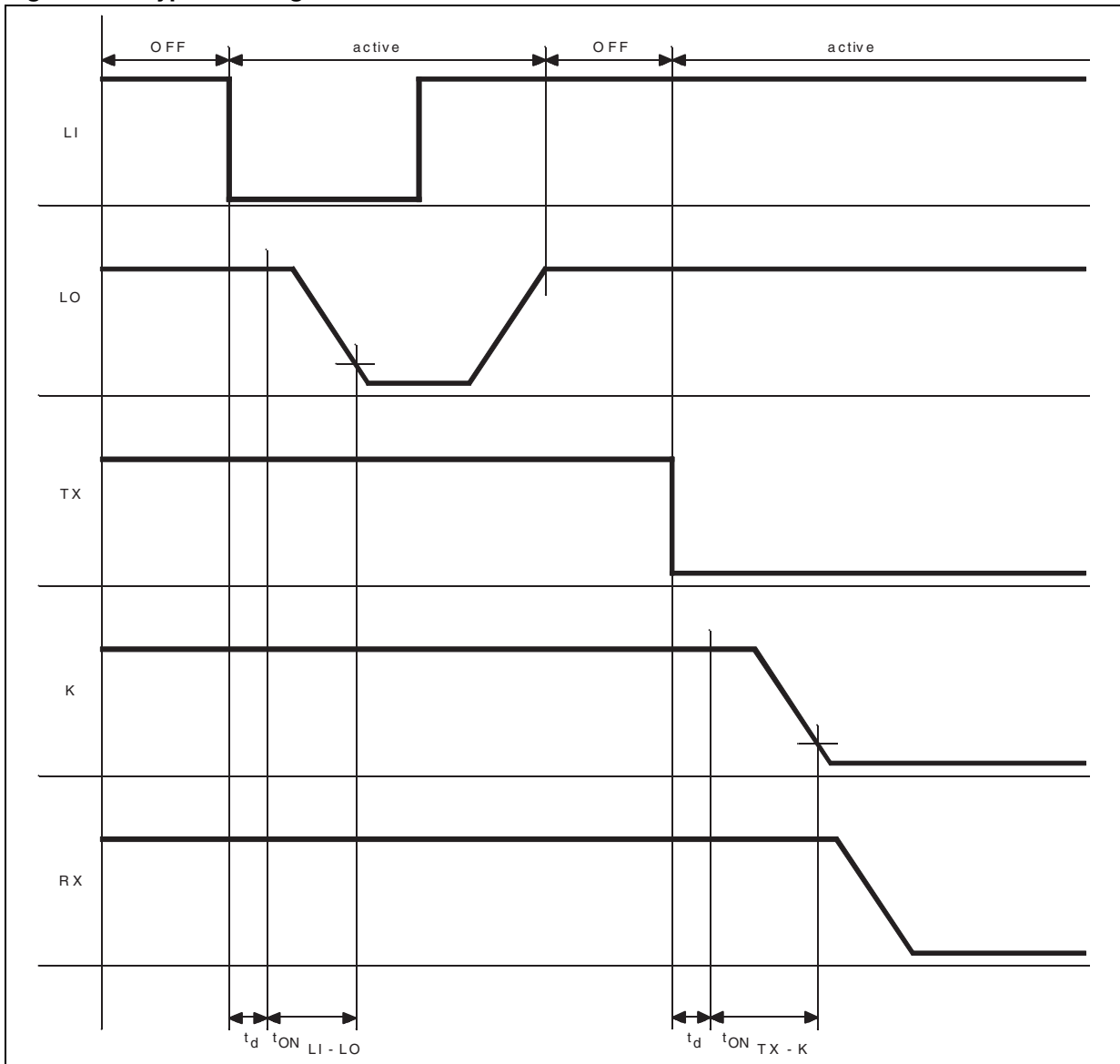


Figure 6. Output characteristics at K, LO, RX

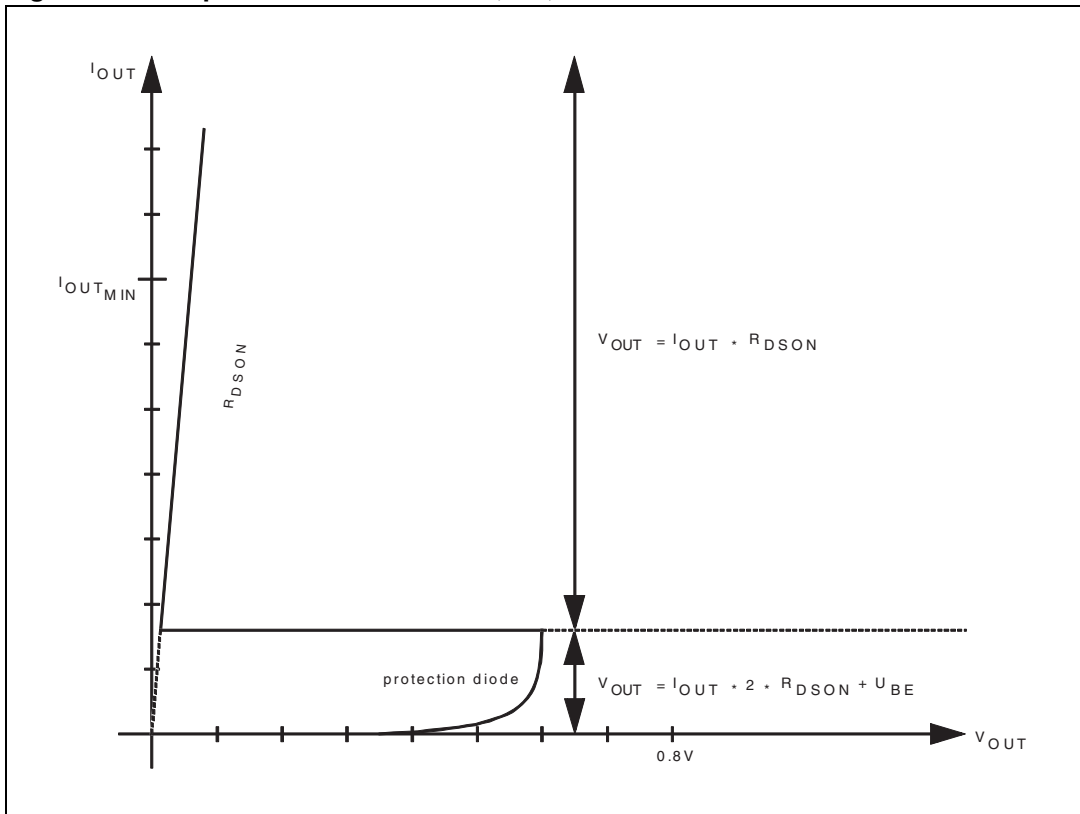


Figure 7. Output signal shape related to output current

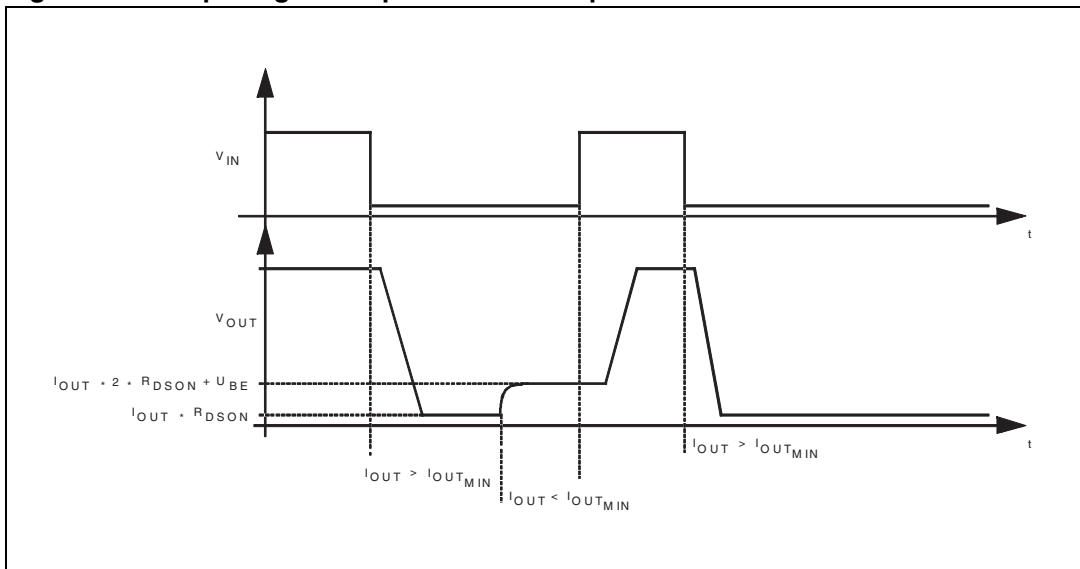


Figure 8. EMS Performance (ISO 9141 bus system)

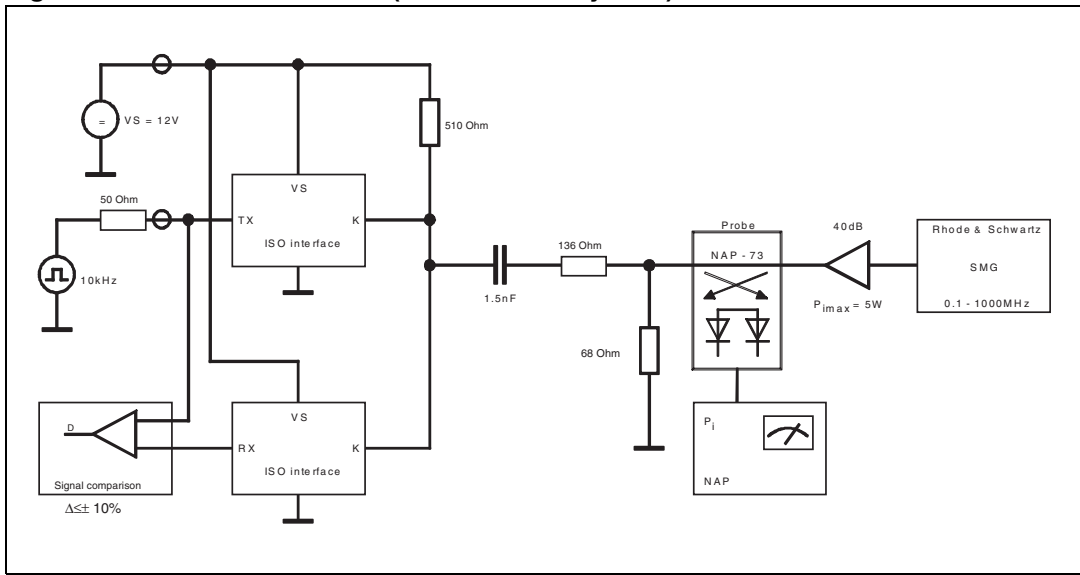
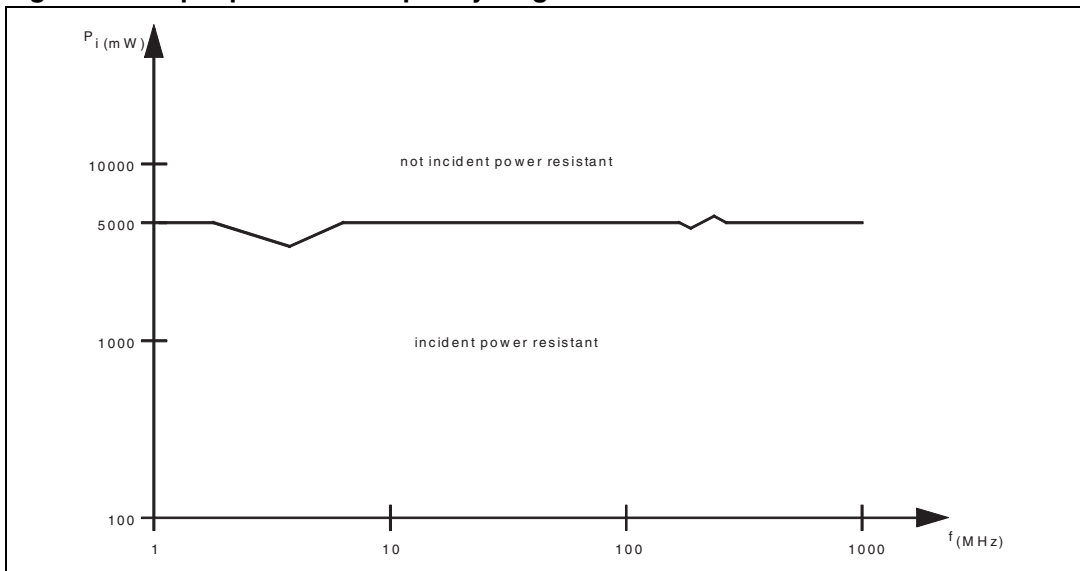


Figure 9. Input power vs. frequency diagram

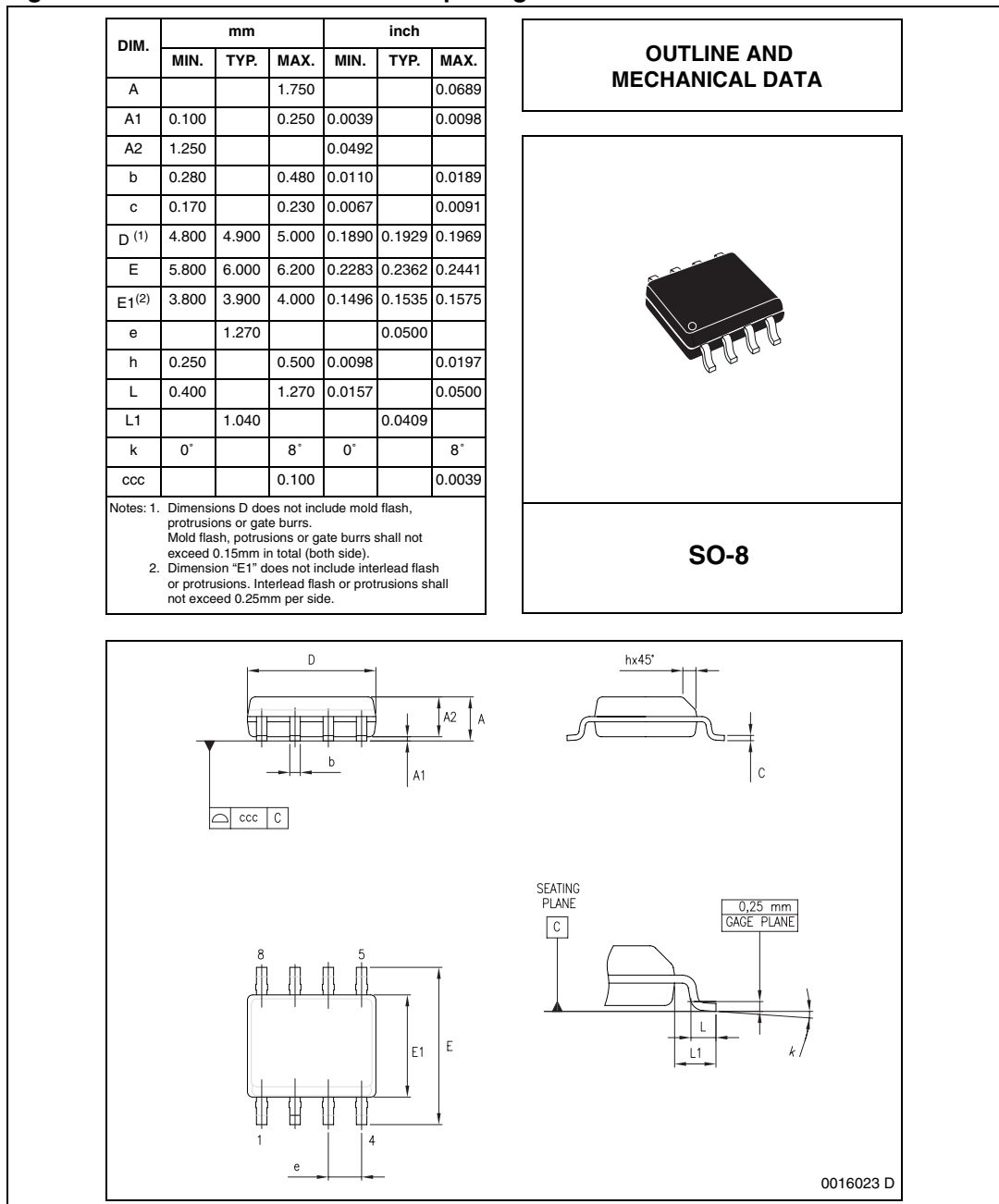


4 Package information

In order to meet environmental requirements, ST (also) offers these devices in ECOPACK[®] packages. ECOPACK[®] packages are lead-free. The category of second Level Interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label.

ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com.

Figure 10. SO8 mechanical data and package dimensions



5 Revision history

Table 6. Document revision history

Date	Revision	Changes
24-Jan-2002	5	Initial release.
07-Nov-2008	6	Document reformatted. Added Table 1: Device summary on page 1 . Updated Section 4: Package information on page 13 .

Please Read Carefully:

Information in this document is provided solely in connection with ST products. STMicroelectronics NV and its subsidiaries ("ST") reserve the right to make changes, corrections, modifications or improvements, to this document, and the products and services described herein at any time, without notice.

All ST products are sold pursuant to ST's terms and conditions of sale.

Purchasers are solely responsible for the choice, selection and use of the ST products and services described herein, and ST assumes no liability whatsoever relating to the choice, selection or use of the ST products and services described herein.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted under this document. If any part of this document refers to any third party products or services it shall not be deemed a license grant by ST for the use of such third party products or services, or any intellectual property contained therein or considered as a warranty covering the use in any manner whatsoever of such third party products or services or any intellectual property contained therein.

UNLESS OTHERWISE SET FORTH IN ST'S TERMS AND CONDITIONS OF SALE ST DISCLAIMS ANY EXPRESS OR IMPLIED WARRANTY WITH RESPECT TO THE USE AND/OR SALE OF ST PRODUCTS INCLUDING WITHOUT LIMITATION IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION), OR INFRINGEMENT OF ANY PATENT, COPYRIGHT OR OTHER INTELLECTUAL PROPERTY RIGHT.

UNLESS EXPRESSLY APPROVED IN WRITING BY AN AUTHORIZED ST REPRESENTATIVE, ST PRODUCTS ARE NOT RECOMMENDED, AUTHORIZED OR WARRANTED FOR USE IN MILITARY, AIR CRAFT, SPACE, LIFE SAVING, OR LIFE SUSTAINING APPLICATIONS, NOR IN PRODUCTS OR SYSTEMS WHERE FAILURE OR MALFUNCTION MAY RESULT IN PERSONAL INJURY, DEATH, OR SEVERE PROPERTY OR ENVIRONMENTAL DAMAGE. ST PRODUCTS WHICH ARE NOT SPECIFIED AS "AUTOMOTIVE GRADE" MAY ONLY BE USED IN AUTOMOTIVE APPLICATIONS AT USER'S OWN RISK.

Resale of ST products with provisions different from the statements and/or technical features set forth in this document shall immediately void any warranty granted by ST for the ST product or service described herein and shall not create or extend in any manner whatsoever, any liability of ST.

ST and the ST logo are trademarks or registered trademarks of ST in various countries.

Information in this document supersedes and replaces all information previously supplied.

The ST logo is a registered trademark of STMicroelectronics. All other names are the property of their respective owners.

© 2008 STMicroelectronics - All rights reserved

STMicroelectronics group of companies

Australia - Belgium - Brazil - Canada - China - Czech Republic - Finland - France - Germany - Hong Kong - India - Israel - Italy - Japan - Malaysia - Malta - Morocco - Singapore - Spain - Sweden - Switzerland - United Kingdom - United States of America

www.st.com

